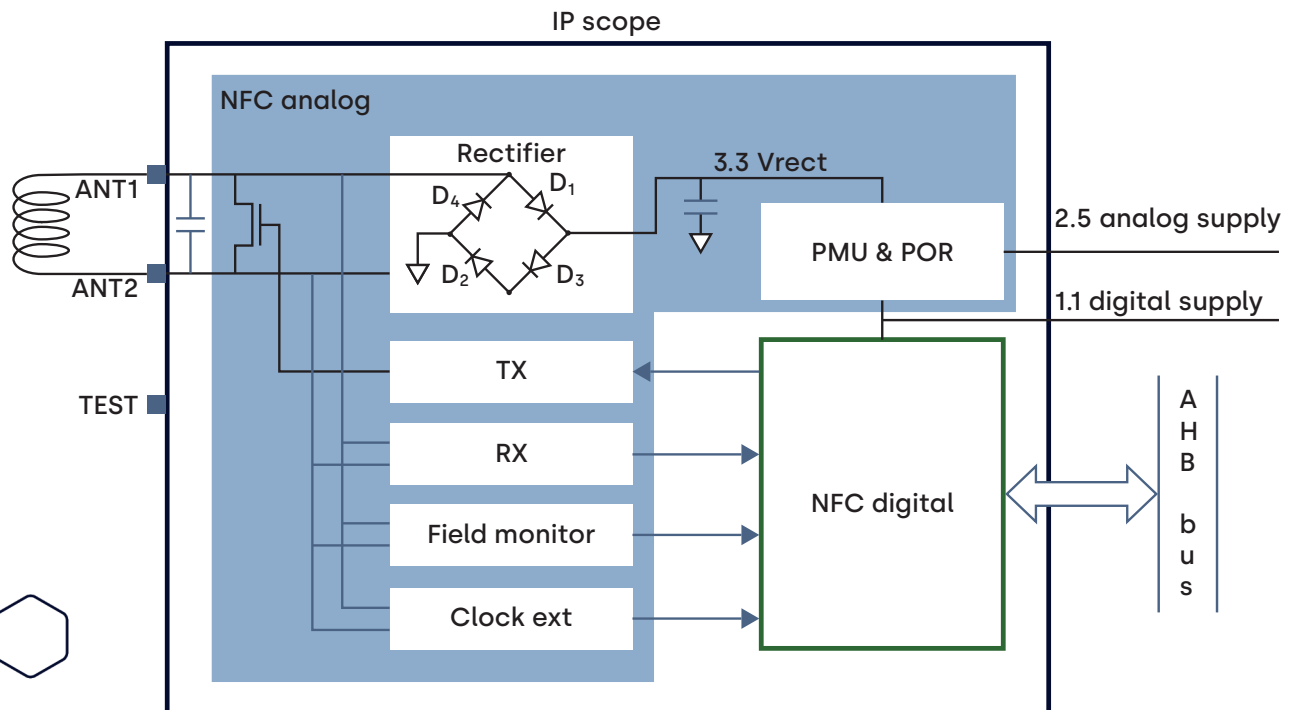


NFC PICC Front-End IP – TSMC 28nm (CLN28HPC+)

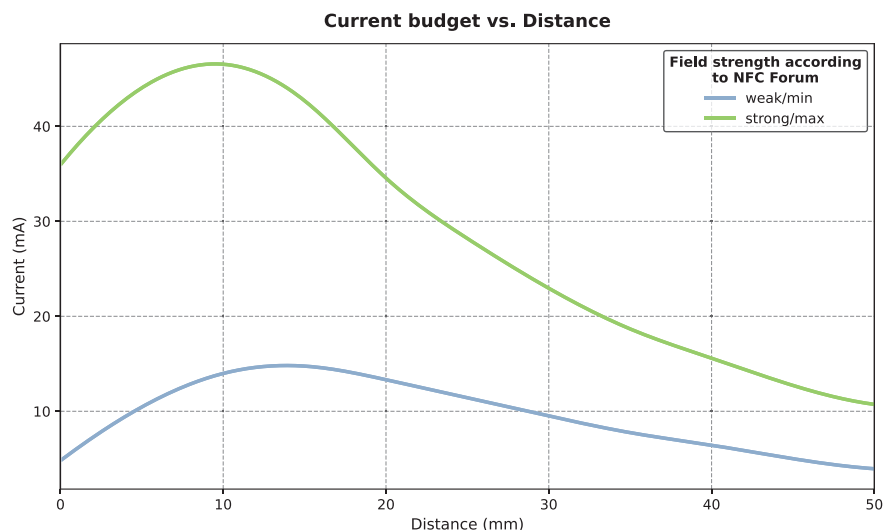
OVERVIEW

OmniChip NFC PICC Front-end IP is a stand-alone, fully passive solution featuring integrated energy harvesting, compatible with all major 13.56 MHz contactless communication standards (ISO/IEC 14443-A/B, ISO/IEC 15693, ISO/IEC 18092).

ARCHITECTURE



ENERGY HARVESTING:



Available harvested current for NFC Forum Poller-0 and Listener-1 setup.

KEY FEATURES:

- ISO/IEC 14443-A/B compliant
 - datarates from 106kbps to 848kbps
 - VHBR up to 3,4MHz in development
- ISO/IEC 18000-3 compliant
- ECMA-373 (NFC-WI) compliant
- NFC Forum compliant
- Silicon area: 0.4 mm² (including internal capacitances)
- AHB interface to the digital core
- **Compatible with 3V and 5V processes**

DELIVERABLES:

- GDSII
- RTL + analog models in SystemVerilog
- Liberty timing models (.lib)
- LEF abstract
- Support in integration

ABOUT OMNICHIP

OmniChip is a semiconductor IP and ASIC design company founded in 2013, bringing together a highly experienced team of IC design experts.

We develop our own semiconductor IP and provide advanced IC design services, with strong expertise in NFC, power management, energy harvesting, and complex mixed-signal systems.

OmniChip

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